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Base Specification, Revision 2.0, PCI Special Interest Group • PXI-5 PXI Express Hardware Specification, Revision 2.0, PXI Systems Alliance ... A fan kit available from National Instruments includes both side and PXI module fan assemblies. Rack Mount Kits

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Perform the following steps to use the C29x PCIe card in the PCIe endpoint mode:

1. Connect the heat sink fan power line to J18.
2. Plug C29x PCIe into the PCIe slot on the motherboard. C29x PCIe supports x1, x2, and x4 configurations. C29x PCIe Card User Guide, Rev. 1, 03/2014 Freescale Semiconductor, Inc. 13

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1. Navigate to `./software/user/example` under the design example directory.
2. Compile the design example application: `$ make`
3. Run the test: `$./intel_fpga_pcie_link_test`

You can run the Intel FPGA IP PCIe link test in manual or automatic mode. — In automatic mode, the application automatically selects the device. The test

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Arria 10 Hard IP for PCI Express - Intel About This Guide This user guide describes the function and operation of the Spartan®-6 FPGA Integrated Endpoint Block for PCI Express® core, including how to design, customize, and implement the core. Guide Contents This manual contains these chapters and appendices: † Chapter 1, Introduction, describes the core and related information, including

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Figure 1. Stratix 10 PCIe Variant with Avalon-ST Interface

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Procedure 1. Align the full-length mini-PCIe module with the full-length slot on the mini-PCIe adapter board. 2. Insert the mini-PCIe module connector into the slot. 3. Using a Phillips screwdriver, insert and tighten the two screws to secure the mini-PCIe module to the board. Page 33:
Configuration

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