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Electronic packaging is the design and production of enclosures for electronic devices ranging from individual semiconductor devices up to complete systems such as a mainframe computer. Packaging of an electronic system must consider protection from mechanical damage, cooling, radio frequency noise emission and electrostatic discharge. Product safety standards may dictate particular features of a consumer product, for example, external case temperature or grounding of exposed metal parts.

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The main objectives of electronic packaging are (a) the protection of the IC chip (or die) and (b) the interconnection of IC chips to other electronic components (i.e., IC chips, printed circuit boards (PCBs), transformers, and connectors) for transfer of electrical signals. An electronic package may be designed to •

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(engineering) The technology of packaging electronic equipment; in current usage it refers to inserting discrete components, integrated circuits, and MSI and LSI chips (usually attached to a lead frame by beam leads) into plates through holes on multilayer circuit boards (also called cards), where they are soldered in place.

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